

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	)
**	) I hereby certify that this paper is being
Dong Ho Lee et al.	) deposited with the United States Postal
	) Service as first class mail, postage
Serial No. 10/613,616	) prepaid, in an envelope addressed to:
	) Commissioner for Patents, P.O. Box
Filed: July 3, 2003	) 1450, Alexandria, VA 22313-1450 on
	) this date:
For: Method of Manufacturing a	)
Semiconductor Wafer	) December 27, 2004
	)
Group Art Unit: 2813	ander In Jamere
Examiner: Chandra P. Chaudhari	Andrew M. Lawrence, Reg. No. 46,130 Attorney for Applicants

## AMENDMENT "A"

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This paper is in response to the official action dated August 25, 2004 (hereafter, "the official action").

The Amendments to the Specification section begins on page 2 of this paper.

The Complete Listing of Claims begins on page 3 of this paper.

The Remarks section begins on page 4 of this paper.